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L1	5335	(702/112,180;703/14,20,23, 27;716/5,16;717/114,132-133,156). ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/10/16 07:50
L2	338	1 and (user with (input debug$3 instruction specif$5)) and (test "initial runs" profiling instrumentation and (data results)) and (rerun repeat recursive feed-through)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/10/16 07:51
L3	995	(ICE FPGA emulation simulation benchmark) and (((profil$3 measurement benchmark test) near4 (result output value data)) same ((reused input (fed adj (back into))) same (run retest repeat subsequent)))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/10/16 07:52
L4	1	2 and 3 and FPGA and (netlist HDL RTL)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/10/16 07:52
L5	0	Gupta.in. and simulation and profiling and VHDL	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/10/16 08:02
L6	30	Gupta.in. and (simulation cosimulat$3) and (profile instrumentation "hot spots")	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/10/16 08:06
L7	6	6 and programmable	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/10/16 08:18
L8	8	("6665855" "6202044" "5937179" "5911059").pn.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/10/16 08:19

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S1	94	(programmable reconfigurable) and ((emulat$4 simulat$4 debug$4 test$3).ab. or (emulat$4 simulat$4 debug$4 test$3).clm.) and (hardware near configuration near3 program) and (Labview FPGA)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/10/14 15:20
S2	15	S1 and ((repeat$4 iterat$4) with (substep task subtask step portion subportion)) and (user near3 input)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/10/14 15:14
S3	85	S1 and (graphical GUI view$3) and (host near4 (computer machine)) and (user near3 input)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/10/14 15:14
S4	13	S3 and ((repeat$4 iterat$4 successive recursiv$4) with (substep task subtask step portion subportion run re-execut$4 rerun))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/10/14 17:54
S5	11	S2 and S4	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/10/14 15:16
S6	85	S3 and (FPGA same simulat$4 emulat$4 test$4)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/10/14 15:17
S7	34	S3 and (FPGA same (simulat$4 emulat$4 test$4)) and compil$3	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/10/14 15:17
S8	1	S2 and S7	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/10/14 15:19

S9	528	FPGA and (netlist RTL) and ((emulat$4 simulat$4 debug$4 test$3).ab. or (emulat$4 simulat$4 debug$4 test$3).clm.)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/10/14 16:41
S10	87	("host computer" and compiler) and ((repeat$4 iterat$4 successive recursiv$4) with (substep task subtask step portion subportion run re-execut$4 rerun)) and (netlist TAPR RTL HDL) and (FPGA FPD "programmable logic")	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/10/14 15:32
S11	7	S3 and S10	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/10/14 15:36
S12	64	Peck.in. and (measur$4 configurable programmable) and (hardware same (program test$4))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/10/14 15:52
S13	23	S12 and (user with (input debug$3 instruction specif$5)).clm.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/10/14 17:52
S14	3	S13 and debug$4.clm.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/10/14 15:59
S15	9	S13 and test$4.clm. and (user near4 input).clm.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/10/14 16:00
S16	6	S15 not S14	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/10/14 16:03

S17		7	(S15 or S16) and (first same second).clm.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/10/14 16:14
S18		1	S12 and ((user near4 (instruction input specif$6)) and first and second and programmable and (debug$4 feed-through)).clm.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/10/14 16:17
S19		3	S12 and ((user near4 (instruction input specif$6)) and (reconfigur$5 programmable) and (debug$4 feed-through)).clm.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/10/14 16:30
S20		7	S17 and (input and user).clm.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/10/14 16:31
S21		7	S20 and (user with (input debug$3 instruction specif$5)).clm.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/10/14 17:52
S22		1	S20 and (user with (input and debug$3)).clm.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/10/14 16:32
S23		71	(S9 or S1 or S4 or S3) not S12 and FPGA and measurement and ((user near3 (change command input specification)) same (tool LABVIEW debugger GUI "user interface" (input adj device)))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/10/14 16:43
S24		30	S23 and compil$4 and debug$3 and (hardware with (configuration reconfig$5 programmable))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/10/14 17:51

S25	18	S24 not ("National Instruments".as. and @pd>="20020806")	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/10/14 16:47
S26	12	S24 not S25	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/10/14 16:53
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S28	0	"37652".apn.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/10/14 16:53
S29	0	"37652".an.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/10/14 16:54
S30	5	"037652".ap.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/10/14 16:54
S31	12	S25 and (("I/O" near4 port) sensor receptor)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/10/14 17:43
S32	12	S31 and (debug$4 emulat$4 simulat$4 test)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/10/14 17:46

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S33	4	S32 and Axis.as. and debug$4 and (user with input)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/10/14 17:47
S34	14	Tseng.in. and compil$4 and debug$3 and (hardware with (configuration reconfig$5 programmable))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/10/14 17:52
S35	0	S34 and (user with (input debug$3 instruction specif$5)) and (profiling instrumentation) and (rerun repeat recursive)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/10/16 07:50
S36	0	S34 and (user with (input debug$3 instruction specif$5)) and (profiling instrumentation)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/10/14 17:54
S37	0	S34 and (user with (input debug$3 instruction specif$5)) and (profil$3 instrumentation)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/10/14 17:54
S38	13	S34 and (user with (input debug$3 instruction specif$5)) and netlist	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/10/14 17:54
S39	12	S38 and ((repeat$4 iterat$4 successive recursiv$4) with (substep task subtask step portion subportion run re-execut$4 rerun))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/10/14 17:55
S40	12	S39 and ((test measurement performance benchmark) near8 (output data result feed))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/10/14 17:58

S41	12	(user same debug$4) and S40	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/10/14 17:58
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S43	49	S42 and compil$4 and ("user interface" and debug$4) and ((benchmark parametric measurement profile performance test) near9 (parameter value data))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/10/14 18:07
S44	45	S43 not ("National Instruments".as. "Peck".in.)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/10/14 18:18
S45	4	S44 and ((instrumentation profiling profile) adj2 (result data metrics))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/10/14 18:15
S46	0	Chen.in. and FPGA and "Business Machines".as. and @rlad<="20030806"	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/10/14 18:11
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S51	14	ICE.ab. and ((instrumentation profiling profile) adj2 (result data metrics))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/10/14 18:13
S52	4	In-circuit-Emulation and debug$4 and ((user developer) adj2 (input specification defined)) and programmable	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/10/14 18:14
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S54	40	S53 and ((FPGA and netlist) or (ICE or "In-Circuit-Emulation")) and user and (performance metrics profile measurement)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/10/14 18:18
S55	218	S53 not ("National Instruments".as. "Peck".in.)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/10/14 18:18
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